

SynthWorksSM



VHDL Training Experts

Jumpstart your VHDL design and verification tasks

Whether it be introductory, verification, or synthesis training, the knowledge you gain will help you finish your next FPGA or ASIC project in a more timely and efficient manner.

Learn from our VHDL design and verification experts

Our instructors have solved difficult design and test coding problems and can answer your questions in detail.

Learn leading edge verification techniques

Learn to use our verification packages for constrained random testing, functional coverage, memories, scoreboards, and interfaces.

Get VHDL hardware experience with our FPGA based Lab board

Vendor independent training

Free yourself from vendor specific techniques. Learn coding styles that are portable and effective for all EDA tools. In class use the tools and/or FPGA of your choice.

Quality that we guarantee

We have been teaching VHDL since 1997 and have kept our training materials under a continual improvement process. All of our classes come with a satisfaction guarantee (details on our website).

Our Courses

Whether you are just getting started or looking to improve your verification or synthesis coding techniques, we have a class for you.

Comprehensive VHDL Introduction - 4 days

Learn VHDL syntax plus the basics of RTL and testbench coding
Students get hardware experience with our FPGA based lab board

VHDL Testbenches and Verification - 5 days

Learn the latest VHDL verification techniques including transaction-based testing, bus functional modeling, self-checking, data structures (linked-lists, scoreboards, memories), directed, algorithmic, constrained random, and coverage driven random testing, and functional coverage.

VHDL Coding for Synthesis - 4 days

Learn VHDL RTL (FPGA and ASIC) coding styles, methodologies, design techniques, problem solving techniques, and advanced language constructs to produce better, faster, and smaller logic.

We offer both on-site and public class sessions

What Our Customers Say

"Emphasized key concepts, labs drove home ideas."

"Excellent insight into unseen/unexpected gotcha's."

"Enjoyed having a designer teach the course."

www.SynthWorks.com

Learn VHDL from a Designer's Perspective with SynthWorks